



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
BORGATTI ET AL.)
)
Serial No. 10/768,401)
)
Filing Date: JANUARY 30, 2004)
)
For: RECONFIGURABLE SIGNAL)
PROCESSING IC WITH AN EMBEDDED)
FLASH MEMORY DEVICE)
)

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop: PGPUB-Drawings
Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Enclosed are six (6) sheets of formal drawings to be
filed in the above-identified patent application.

Respectfully submitted,

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CERTIFICATE OF MAILING

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